

THE PAINLESS ROUTE IN PYTHON TO FAST AND SCALABLE MACHINE LEARNING

Victoriya Fedotova, Frank Schlimbach

THE REALITY OF "DATA CENTRIC COMPUTING"

Software Challenges:

Performance Limited	 Software is slow and single-node for many organizations Only sample a small portion of the data
Productivity Limited	 More performant/scalable implementations require significantly more development & deployment skills & time
Compute Limited	 Performance bottleneck often in compute, not storage/memory

A typical data scientist analyzes only a small portion of data that they think has the most potential of bringing the great insights. This means they may miss out on valuable insights from the remaining bigger portion of the data — insights that may be crucial for the business.

Optimization Notice

PRODUCTIVITY WITH PERFORMANCE VIA INTEL® PYTHON*

Intel[®] Distribution for Python*



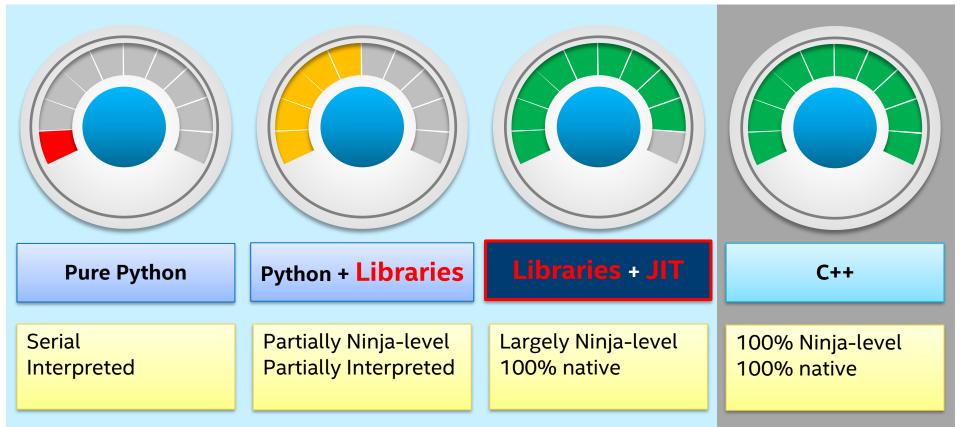
Easy, out-of-the-box access to high performance Python

- Prebuilt accelerated solutions for data analytics, numerical computing, etc.
- Drop in replacement for your existing Python. No code changes required.

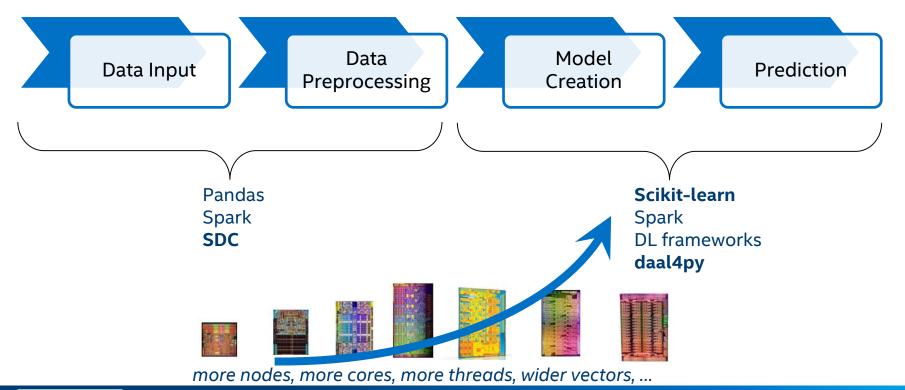
Learn More: software.intel.com/distribution-for-python



TWO INGREDIENTS TO GET CLOSE-TO-NATIVE PERFORMANCE IN PYTHON



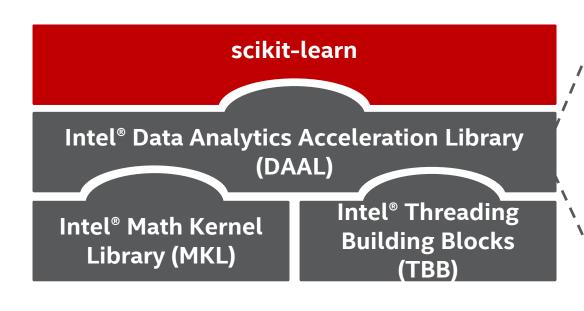
DATA ANALYSIS AND MACHINE LEARNING



Optimization Notice

INTEL® DATA ANALYTICS ACCELERATION LIBRARY (DAAL)

ACCELERATING MACHINE LEARNING



Try it out! conda install -c intel scikit-learn

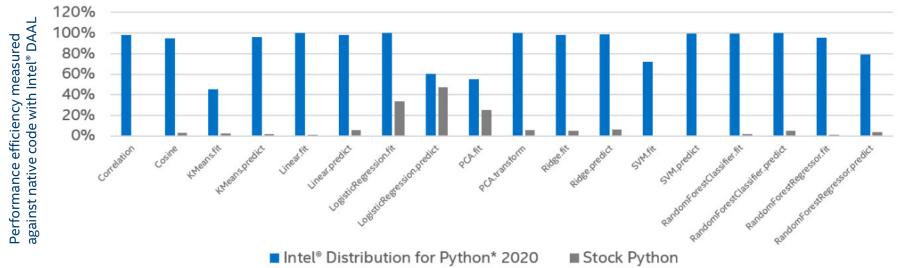
>Efficient memory layout

- Chunking for optimal cache performance
- Computations mapped to most efficient matrix operations (in MKL)
- Parallelization via TBB
 Vectorization

timization Notice



CLOSE TO NATIVE SCIKIT-LEARN PERFORMANCE WITH INTEL PYTHON 2020 COMPARED TO STOCK PYTHON PACKAGES ON INTEL® XEON PROCESSORS



Configuration: Testing by Intel as of November 27, 2019. Stock Python: Python 3.7.5 h_0371630_0 installed from conda, numpy 1.17.4, numba 0.46.0, llvmlite 0.30.0, scipy 1.3.2, scikit-learn 0.21.3 installed from pip; Intel Python: Python 3.7.5 h_0371630_0 installed from conda, numpy 1.17.4, numba 0.46.0, llvmlite 0.30.0, scipy 1.3.2, scikit-learn 0.21.3 installed from pip; Intel Python: Intel® Distribution for Python* 2020 Gold: Python 3.7.4 hf484d3e_3, numpy 1.17.3 py37ha68da19_4, mkl 2020 intel_133, mkl_fft 1.0.15 py37ha68da19_3, mkl_random 1.1.0 py37ha68da19_0, numba 0.45.1 np117py37_1, llvmlite 0.29.0 py37hf484d3e_9, scipy 1.3.1 py37ha68da19_2, scikit-learn 0.21.3 py37ha68da19_14, daal 2020 intel_133, daal4py 2020 py37ha68da19_4; Cent OS Linux 7.3.1611, kernel 3.10.0-514.eI7.x86_64; Hardware: Intel Xeon® Platinum® 8280 CPU @ 2.70 GHz (2 sockets, 28 cores/socket, HT:off), 256 GB of DDR4 RAM, 16 DIMMs of 16 GB@2660MHz.

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark* and MobileMark*, are measured using specific computer systems, components, software, operations, and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products. For more complete information visit https://www.intel.com/benchmarks

Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice. Notice revision #20110804

Optimization Notice

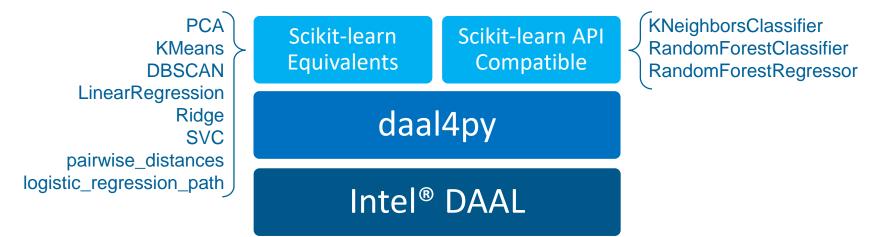
ACCELERATING SCIKIT-LEARN THROUGH DAAL4PY

> python -m daal4py <your-scikit-learn-script>

Monkey-patch any scikit-learn on the command-line

import daal4py.sklearn
daal4py.sklearn.patch_sklearn(`kmeans')

Monkey-patch any scikit-learn programmatically

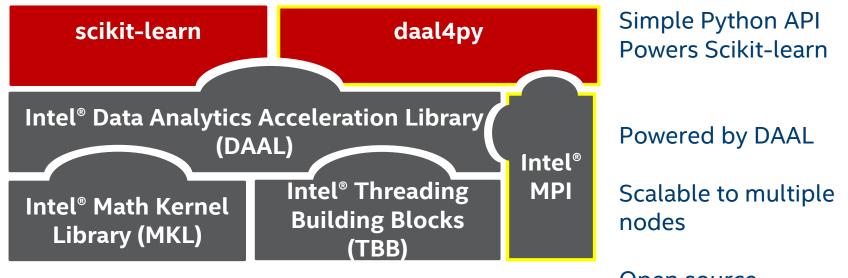


Scikit-learn with daal4py patches applied passes Scikit-learn test-suite

Optimization Notice



SCALING MACHINE LEARNING BEYOND A SINGLE NODE



Open source

Try it out! conda install -c intel daal4py

Optimization Notice



K-MEANS USING SCIKIT-LEARN AND DAAL4PY

Scikit-learn

```
from sklearn.cluster import KMeans
import pandas as pd
```

data = pd.read_csv("./kmeans.csv")

```
algo = KMeans(n_clusters=20,
init='k-means++', max_iter=5)
```

```
result = algo.fit(data)
```

result.labels_
result.cluster_centers_

daal4py

result.centroids

```
from daal4py import kmeans_init, kmeans
import pandas as pd
data = pd.read csv("./kmeans.csv")
                                             # Load the data
init = kmeans init(nClusters=20,
                                             # Compute initial
                                             # centroids
  method="plusPlusDense").compute(data)
                                             # Configure K-means
algo = kmeans(nClusters=20,
  maxIterations=5, assignFlag=True)
                                             # main object
                                             # Compute the
result = algo.compute(data,
                                             # clusters and labels
                       init.centroids)
                                             # Print the results
result.assignments
```



11

DISTRIBUTED K-MEANS USING DAAL4PY

from daal4py import kmeans_init, kmeans, daalinit, daalfini, my_procid
import pandas as pd

Optionally initialize distributed execution environment
daalinit()

```
# Load the data. Daal4py accepts data as CSV files, numpy arrays or pandas dataframes
data = pd.read_csv("./kmeans_dense_{}.csv".format(my_procid() + 1))
```

```
# compute initial centroids
init_res = kmeans_init(nClusters=10, method="plusPlusDense", distributed=True).compute(data)
```

```
# configure kmeans main object: we also request the cluster assignments
algo = kmeans(nClusters=10, maxIterations=25, distributed=True)
```

```
# compute the clusters/centroids
result = algo.compute(data, init_res.centroids)
```

<mark>daalfini()</mark>

mpirun -n 4 python kmeans_distributed.py

Optimization Notice



DAAL4PY API GENERATION

#include <mpi.h> /* Serialize partial results required by step 2 */ #include "daal.h" services::SharedPtr<byte> serializedData; #include "service.h" InputDataArchive dataArch; localAlgorithm.getPartialResult()->serialize(dataArch); size_t perNodeArchLength = dataArch.getSizeOfArchive(); using namespace std: using namespace daal: tynedef float algorithmFPType: Semi-automatic API generation process: node called compute() equal number of times */ Parse C++ headers to generate Cython code. /* Input data set parameters * const size t nBlocks = 4; size t nFeatures: Use jinja2 to generate Python classes for algorithms, models, MPLCHAR, HPLCONE, MPLCONE, MPLC int rankId, comm_size; #define mpi_root 0 const string fileNames[] = { "./pca_1. Festults", etc.csv", "./pca_4.csv" }; int main(int argc, char * argv[]) /* Create an algorithm for principal component analysis using the SVD method on the master node */ pca::Distributed<step2Master. algorithmFPTvpe, pca::svdDense> masterAlgorithm; checkArguments(argc, argv, 4, &fileNames[0], &fileNames[1], &fileNames[2], &fileNames[3]); for (size_t i = 0; i < nBlocks; i++)</pre> MPI_Init(&argc, &argv); /* Deserialize partial results from step 1 */ OutputDataArchive dataArch(serializedData.get() + perNodeArchLength * i, perNodeArchLength); MPI_Comm_size(MPI_COMM_WORLD, &comm_size); MPI Comm rank(MPI COMM WORLD, &rankId); services::SharedPtr<pca::PartialResult<pca::sydDense> > dataForStep2FromStep1 = services::SharedPtr<pca::PartialResult<pca::svdDense> >(new pca::PartialResult<pca::svdDense>()); /* Initialize FileDataSource<CSVFeatureManager> to retrieve the input data from a .csv file */ dataForStep2FromStep1->deserialize(dataArch); FileDataSource<CSVFeatureManager> dataSource(datasetFileNames[rankId], DataSource::doAllocateNumericTable, /* Set local partial results as input for the master-node algorithm */ aSource::doDictionarvFromContext); masterAlgorithm.input.add(pca::partialResults. dataForStep2FromStep1); /* Retrieve the input data 100X fewer LOC for multi-node algorithms dataSource.loadDataBlock() /* Create an algorithm for pca::Distributed<step1Local, algorithmFPType, pca::svdDense> localAlgorithm; /* Retrieve the algorithm results */ pca::ResultPtr result = masterAlgorithm.getResult(); /* Set the input data set to the algorithm */ /* Print the results */ localAlgorithm.input.set(pca::data.dataSource.getNumericTable()); printNumericTable(result->get(pca::eigenvalues), "Eigenvalues:"); printNumericTable(result->get(pca::eigenvectors), "Eigenvectors:"); /* Compute PCA decomposition */ localAlgorithm.compute(); MPI Finalize(); return 0:

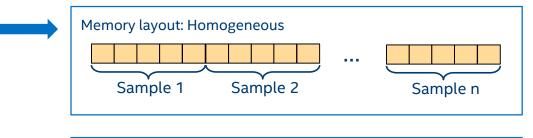
Optimization Notice

EFFECTIVE DATA TRANSFER: PYTHON ↔ NATIVE

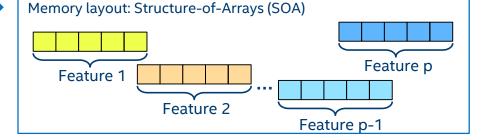


- numpy.ndarray
 - Homogeneous dense array

DAAL data type



- pandas.DataFrame
 - Heterogeneous data



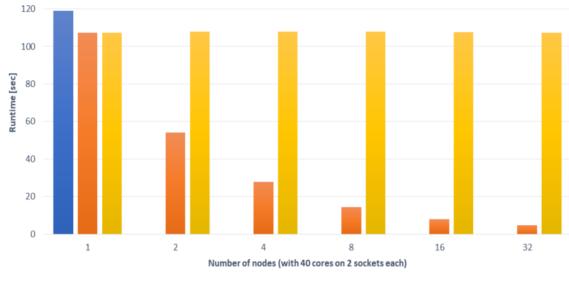
daal4py mostly avoids data copies and works optimally with various data layouts

Optimization Notice

STRONG AND WEAK SCALING VIA DAAL4PY

daal4py K-Means Distributed Scalability

Hard Scaling: Fixed input: 16M observations, 300 features, 10 clusters Weak Scaling: 16M observations and 300 features per node



On a 32-node cluster (1280 cores) daal4py computed K-Means (10 clusters) of 1.12 TB of data in 107.4 seconds and 35.76 GB of data in 4.8 seconds.

Configuration: Intel® Xeon® Gold 6148 CPU @ 2.40GHz, EIST/Turbo on 2 sockets, 20 cores per socket, 192 GB RAM, 16 nodes connected with Infiniband, Oracle Linux Server release 7.4, using 64-bit floating point numbers

Batch Mode (single node base-line)

Hard Scaling, 2 process per node

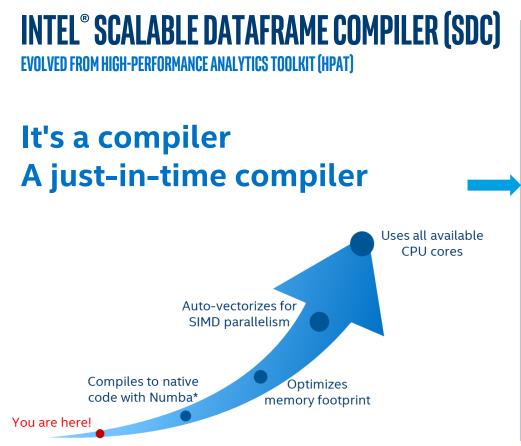
Weak Scaling; 2 processes per node

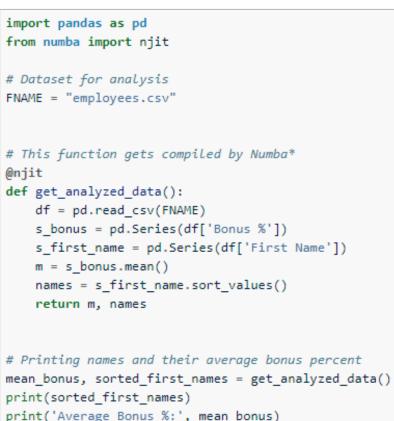


Optimization Notice

140

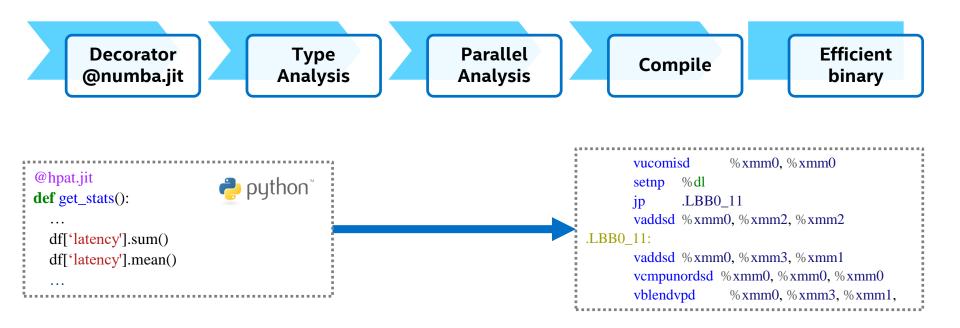
INTEL[®] SCALABLE DATAFRAME COMPILER (SDC)





Optimization Notice

COMPILATION PIPELINE (HIGH-LEVEL VIEW)



Optimization Notice



BASIC WORKFLOW EXAMPLE

```
import numpy as np
import pandas as pd
from numba import njit
# This function gets compiled by Numba*
@njit
def get analyzed data(file name):
    df = pd.read csv(file name,
                     dtype={'Bonus %': np.float64, 'First Name': str},
                     usecols=['Bonus %', 'First Name'])
    s bonus = pd.Series(df['Bonus %'])
    s first name = pd.Series(df['First Name'])
    m = s bonus.mean()
    names = s first name.sort values()
    return m, names
mean_bonus, sorted_first_names = get_analyzed data('employees.csv')
print(sorted first names)
print('Average Bonus %:', mean bonus)
```



INTEL® SDC AND NUMBA* LIMITATION: TYPE STABILITY

Input code to SDC must be statically compilable (type stable)

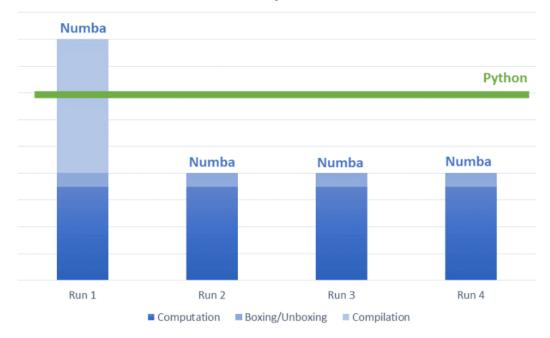
• Dynamically typed code examples (rare in analytics):

Untypable variable	Unresolvable function	Nonstatic DataFrame schema
<pre>if flag1: a = 2 else:</pre>	<pre>if flag2: f = np.zeros else: f</pre>	<pre>if flag3: df = pd.DataFrame({ 'A': [1,2,3]})</pre>
<pre>a = np.ones(n) if isinstance(a, np.ndarray): doWork(a)</pre>	<pre>f = np.ones b = f(m)</pre>	<pre>else: df = pd.DataFrame({ 'A': ['a', 'b', 'c']}) b = f(m)</pre>



GETTING PERFORMANCE WITH INTEL® SDC

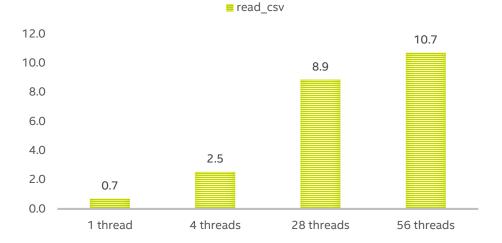
Execution times: Python* vs. Numba*



- Compile parts of code where parallelism resides
- Compile functions that are called multiple times
- Minimize number of columns in dataframes in the regions being compiled

Intel SDC Performance – read_csv

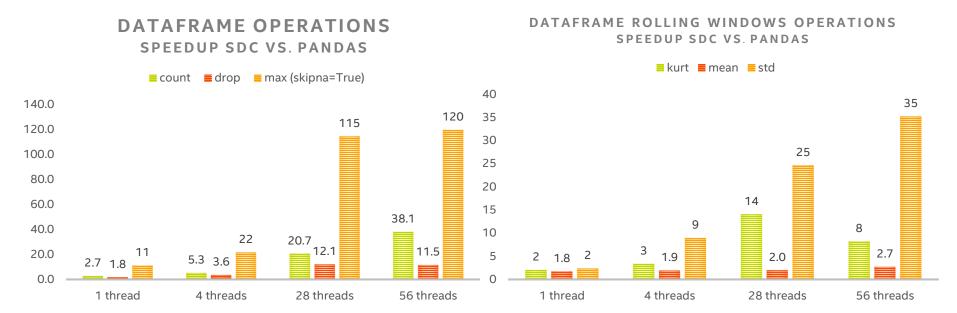
INPUT/OUTPUT OPERATIONS SPEEDUP SDC VS. PANDAS



Intel[®] SDC Beta, Numba* 0.48, Pandas* 0.25.3 Intel[®] Xeon[™] Platinum 8280L, 2.7 GHz, 2x28 cores, Hyperthreading=on, Turbo Mode=on



Intel SDC Performance – Dataframes



Intel[®] SDC Beta, Numba* 0.48, Pandas* 0.25.3

Intel® Xeon™ Platinum 8280L, 2.7 GHz, 2x28 cores, Hyperthreading=on, Turbo Mode=on

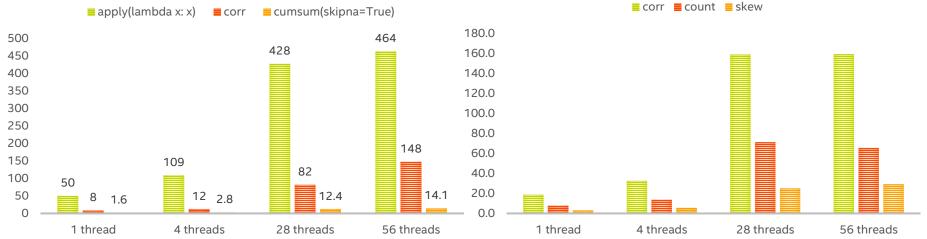
Optimization Notice



Intel SDC Performance – Series

SERIES OPERATIONS SPEEDUP SDC VS. PANDAS

SERIES ROLLING WINDOWS OPERATIONS SPEEDUP SDC VS. PANDAS



Intel® SDC Beta, Numba* 0.48, Pandas* 0.25.3

Intel® Xeon™ Platinum 8280L, 2.7 GHz, 2x28 cores, Hyperthreading=on, Turbo Mode=on

Optimization Notice



INTEL® SDC FUNCTIONALITY

Coming soon: time series and categoricals

Operations	 Python/Numpy/Pandas* basics Statistical operations (max, std, median,) Relational operations (filter, groupby) Rolling window (rolling)
Data	 Missing value Dates ASCII/Unicode strings Data-Frames, Series, Lists, Dictionaries, Tuples

Interoperability • I/O integration (CSV)

Optimization Notice



INTEL[®] SCALABLE DATAFRAME COMPILER (SDC)

EVOLVED FROM HIGH-PERFORMANCE ANALYTICS TOOLKIT (HPAT)

Open source project

- <u>https://github.com/IntelPython/sdc</u>
- <u>https://intelpython.github.io/sdc-doc/latest/index.html</u>

In Beta till end of 2020

Available as conda packages and pip wheels (Python 3.6/3.7, Windows/Linux)

- conda install -c intel/label/beta sdc
- pip install -i https://pypi.anaconda.org/intel/label/beta/simple sdc

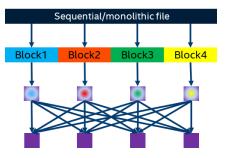


26



More Pandas features

Auto-scale-out to clusters of workstations



Compiling to and running on GPUS/accelerators



COMPILING TO AND RUNNING ON GPUS/ACCELERATORS

```
@dppy.kernel
2
3
   □def data parallel sum(a, b, c):
        i = dppy.get global id(0)
        c[i] = a[i] + b[i]
4
```

```
@numba.vectorize(nopython=True)
 2
    □def cndf2(inp):
 3
         out = 0.5 + 0.5 * \text{math.erf}((\text{math.sgrt}(2.0)/2.0) * \text{inp})
         return out
 4
 5
 6
     @numba.njit(parallel=True, fastmath=True)
    def blackscholes(sptprice, strike, rate, volatility, timev):
 8
         logterm = np.log(sptprice / strike)
         powterm = 0.5 * volatility * volatility
 9
10
         den = volatility * np.sgrt(timev)
11
         d1 = (((rate + powterm) * timev) + logterm) / den
12
         d2 = d1 - den
13
         NofXd1 = cndf2(d1)
14
         NofXd2 = cndf2(d2)
15
         futureValue = strike * np.exp(- rate * timev)
16
         c1 = futureValue * NofXd2
17
         call = sptprice * NofXd1 - c1
18
         put = call - futureValue + sptprice
19
         return put
```

1 with device context(gpu, 0): black scholes(SP, S, R, V, T)

2



END-TO-END PERFORMANCE OF ANALYTICS

daal4py and SDC help accelerate, scale-up and scale-out the entire analytics process in Python from preprocessing through machine learning

- <u>https://anaconda.org/intel</u>
- <u>https://software.intel.com/distribution-for-python</u>
- <u>https://intelpython.github.io/daal4py</u>
- <u>https://github.com/IntelPython/sdc</u>
- <u>https://medium.com/intel-analytics-software</u>



LEGAL DISCLAIMER & OPTIMIZATION NOTICE

Performance results are based on testing as of November 27, 2019, May 18, 2020 and may not reflect all publicly available security updates. See configuration disclosure for details. No product can be absolutely secure.

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products. For more complete information visit intel.com/benchmarks.

INFORMATION IN THIS DOCUMENT IS PROVIDED "AS IS". NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO THIS INFORMATION INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

Copyright © 2020, Intel Corporation. All rights reserved. Intel, Xeon, Core, and the Intel logo are trademarks of Intel Corporation in the U.S. and other countries.

Optimization Notice

Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice. Notice revision #20110804

Optimization Notice

